

PATENT NUMBER

O.L.P.E.

PATENT DATE

CONT/PRIOR
D

CLASS

SUBCLASS

ART UNIT

EXAMINER

[illegible]

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David Liu

High voltage MOS devices with high gated-diode breakdown voltage and punch-through voltage

PTO-2040
12/89

ORIGINAL

CROSS REFERENCE(S)

CLASS:

SUBCLASS

CLASS

SUBCLASS (ONE SUBCLASS PER BLOCK)

INTERNATIONAL CLASSIFICATION

☐ Continued on Issue Slip Inside File Jacket

☐ **TERMINAL
DISCLAIMER**

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____

☐ The terminal ____ months of this patent have been disclaimed.

DRAWINGS

Sheets Drwg.

Figs. Drwg.

Print Fig.

CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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